Due: Apr 6, 2016 11:55 pm
Number of resubmissions allowed: Unlimited
Accept Resubmission Until: Jun 6, 2016 11:50 pm
Status: Not Started
Grade Scale: Points (max 80.0)

Assignment Instructions
Chapter 1: 2, 3, 10, 15, 16, 17
Chapter 3: 1, 2, 3, 4, 5

Supplemental problems:
S1: Draw the transistor level diagram of a 3-input AND gate. Be sure to indicate Vcc and GND with the property symbols.
S2: A) Given the Boolean expression, draw the transistor level diagram using the brute force procedure from class.

\[ Q = AB'C' + A'B'C + A'BC' + AB'C. \]

Submission
This assignment allows submissions by attaching documents only. Use the Add Attachments button below to attach 1 or more documents.

Attachments
No attachments yet

Add Attachments

Honor Pledge: I have neither given nor received aid on this assignment. ☐
(You must respond to submit your assignment.)