



LD IF ID ~~EX~~ M WR

M IF ID ID M1 M2 M3 M4 M5 M6 M7 M8

A IF IF ID ID ID ID ID AI

S IF IF IF IF IF ID

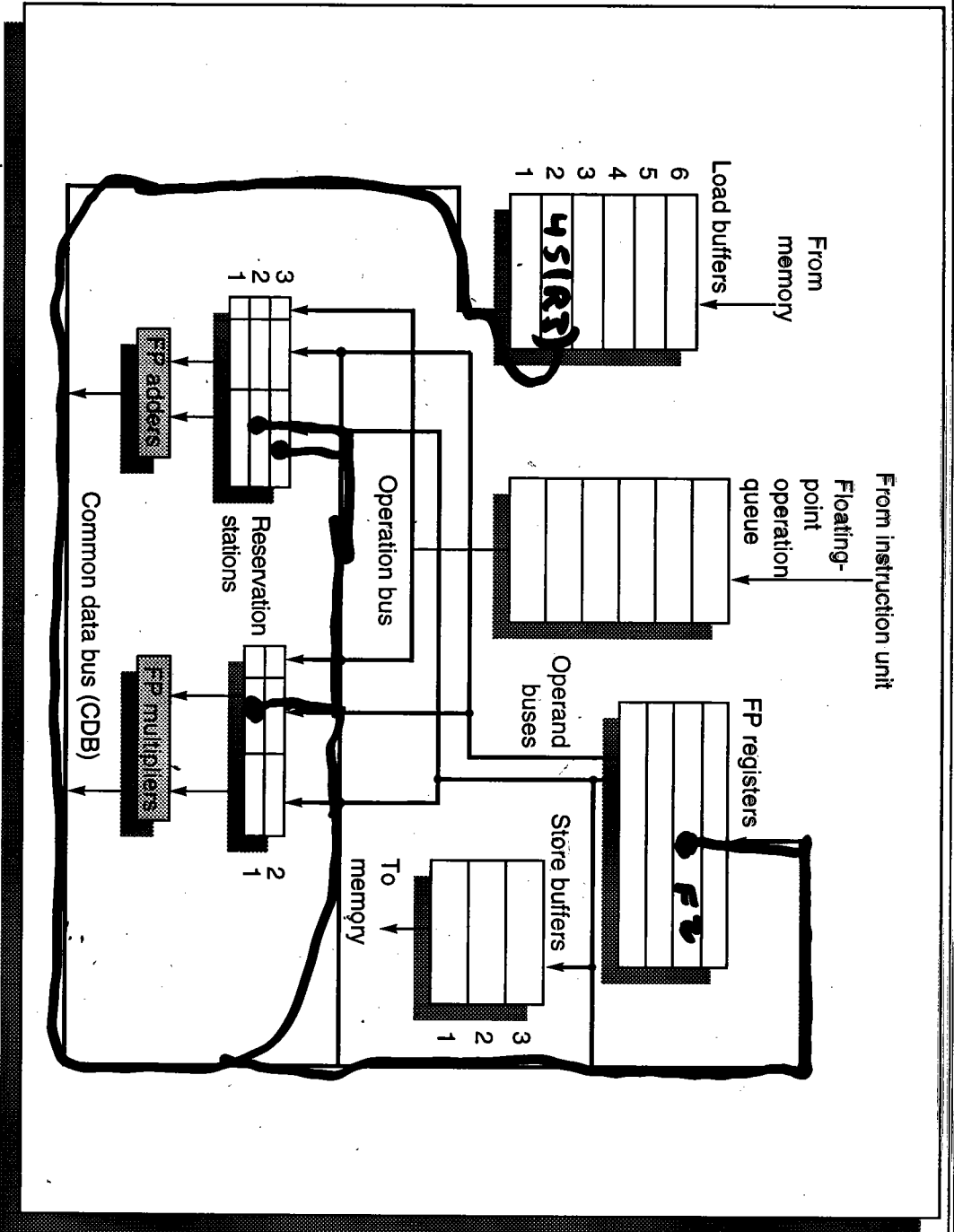


FIGURE 4.8 The basic structure of a DLX FP unit using Tomasulo's algorithm.

